TI-31089



APPLICATION FOR UNITED STATES PATENT DECLARATION AND POWER OF ATTORNEY



As a below named and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 09/876,292, filed 06/07/01; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:				
Additional n-Type LDD/Pocket Implant for Improving Short-Channel NMOS ESD Robustness				
OWER OF ATTORNEY: I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER 23494 TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH				
SEND CORRESPONDENCE TO: Gary C. Honeycutt Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265			DIRECT TELEPHONE CALLS TO: Gary C. Honeycutt (972) 238-7160	
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COUNTRY OF CITIZENSHIP: India		COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP: Korea	
SIGNATURE OF INVENTOR:		SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:	
DATE: 08 - 06 - 01		DATE: x 08-06-01	DATE: x 8-6-4/	